

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(PTO-1449)

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50246-068

 SERIAL NO.
**Reissue of U.S. Patent
5,598,525**

 APPLICANT
Robert M. NALLY, et al.

 FILING DATE
January 23, 1995

 GROUP
2671

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>m</i>	A 5,406,306	4/11/95	Siann et al.	345	544	
<i>m</i>	B 5,608,864	3/4/97	Bindlish et al.	345	558	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>m</i>	C	Shandle, Jack, "Windows Accelerator Chip Provides Multimedia Port", Electronic Design, June 24, 1993, pp. 45, 46, 48-50.
	D	"Spitfire 64-bit Multimedia GUI Accelerator-Preliminary Specification", Oak Technology, September 1994.
	E	Article entitled "Industry's first Video Cache DAC", publication unavailable, date unavailable.
	F	Wilson, Ron, "Brooktree finishes video set", Electronic Engineering Times, February 1, 1993, pg. 57.
	G	"Chip Designer Brooktree Corp Comes Out with Two Devices to Facilitate Cheap Desktop Video Design", Computergram International; February 12, 1993; pg. N/A.
	H	Brooktree Advance Information for 135 MHz Monolithic CMOS Video Cache DAC™, Brooktree Corporation, October 15, 1993, pp. 1-55.
	I	Harney, Kevin et al., "82750DB Programming Guide", Intel Corporation, January 7, 1991, pp. 1-63.
	J	"Multimedia and Supercomputing Processors", Intel Corporation, 1991.
	K	"Sony Picks Parallax Video Boards", Computergram International, ATI020575.
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	M	"VIPER Video Image Processor Data Book", Tseng Labs, ATI002393-002487.
	N	"Parallax 1280 Reference Manual", Parallax Graphics, ATI32068-32766.
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	S	1280 Circuit Schematics, ATI018984-97.
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	U	Harney, Kevin et al., "The i750® Video Processor: A Total Multimedia Solution", Digital Multimedia Systems, Communications of the ACM, April 1991, Vol. 34, No. 4, pp. 65-78.

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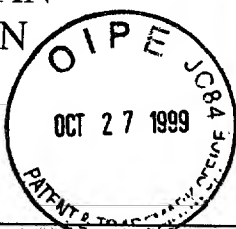
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DATE CONSIDERED

8/1/01

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09/374,041

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Robert M. NALLY, et al.

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August 13, 1999

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2722 2671

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>m</i>	"110 MHz Monolithic CMOS Video CacheDAC™", Product Overview ATI019895-019958.
	"110 MHz Monolithic CMOS Video CacheDAC™", Product Overview ATI019959-020014.
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	"CL-PX2070: Preliminary Data Book", July 1993, ATI019027-ATI019142.
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<i>↓</i>	"CL-PX2080: Preliminary Product Bulletin", August 1992, ATI020381-ATI020384.

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DATE CONSIDERED

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>Am</i>	5,559,954	9/24/96	Sakoda, et al.	345	576	
	5,640,332	6/17/97	Baker, et al.	345	559	
	5,097,257	3/17/92	Clough, et al.	345	213	
	5,546,531	8/13/96	Lippincott	345	547	
	5,335,321	8/2/94	Harney, et al.	345	503	
	4,599,611	7/8/86	Bowker, et al.	345	636	
	3,618,035	4/17/69	Simms, et al.	709	204	
	3,673,324	6/27/72	Ito, et al.	348	584	
	4,498,098	2/5/85	Stell	348	510	
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	5,557,302	9/17/96	Levinthal, et al.	345	506	
	5,220,312	6/15/93	Lumelsky, et al.	345	563	
	5,291,188	3/1/94	McIntyre, et al.	345	573	
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	5,335,321	8/2/94	Harnoy, et al.			
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	3,673,324	6/27/72	Ito, et al.			
	4,498,098	2/5/85	Stell			
	4,425,581	1/10/84	Schweppe, et al.			
	5,752,010	5/12/98	Herbert			
	5,557,302	9/17/96	Levinthal, et al.			
	5,220,312	6/15/93	Lumelsky, et al.			
	5,291,188	3/1/94	McIntyre, et al.			
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<i>m</i>	"SPITFIRE- 64-bit Multimedia GUI Accelerator-OTI-64107 Preliminary Specification," Oak Technology, Jan. 14 1994, ATI18036-018197 (January 1994 Spec.)
<i>m</i>	"Intel-ATI in Race Against VESA Bus," Electronic Engineering Times, ATI026182-026183.
<i>u</i>	"Intel's i750 ® Video Processor—The Programmable Solution," Manepally, et al. Intel

